

Time	ROOM 1			ROOM 3			ROOM 5		
08:30:00				08:30-09:00 Session 1 Welcome Session					
08:45:00									
09:00:00				09:00-10:00 Session 2 [Keynote] DSD 1 Philipp Mundhenk					
09:15:00									
09:30:00									
09:45:00									
10:00:00									
10:15:00									
10:30:00	10:30-12:00 Session 3A FPGA HW Architectures (Session Chair: Small Njar)	10:30:00	Hans Jakob Damsgaard, Aleksandr Ometov and Jari Nummi Generating CGRA Processing Element Hardware with CGRAgen	10:30:00	Agathe Archet, Nicolas Ventroux, Nicolas Gac and François Orléans Energy-efficient use of an embedded heterogeneous SoC for the inference of CNNs	10:30:00	Nikolaos Athanasios Anagnostopoulos, Nico Meais, Simon Böttger, Martin Hartmann, Ali Mohamed, Sascha Herrmann, Stefan Katzenbeisser, Stavros Stavrinides and Tolga Arul A Method to Construct Efficient Carbon-Nanotube Based Physical Unclonable Functions and True Random Number Generators		
10:45:00									
11:00:00		11:00:00	Stefano Marti, Enis Mustafa, Giacomo Blisson, Pratyush Anand, Philipp Fabritius, Tilman Esslinger and Abdulhadi Akin FPGA-based real-time laser beam profiling and stabilization system for quantum simulation applications	11:00:00	Dominika Przewlocka-Rus and Tomasz Kryjak Power-of-Two Quantized YOLO Network for Pedestrian Detection with Dynamic Vision Sensor	11:00:00	Nico Meais, Tolga Arul, Nikolaos Athanasios Anagnostopoulos, Florian Frank, Simon Böttger, Martin Hartmann, Sascha Herrmann, ERF Bilge Kaun and Stefan Katzenbeisser Spatial Correlation in Weak Physical Unclonable Functions: A Comprehensive Overview		
11:15:00									
11:30:00		11:30:00	Puñi Dhilteswarano, Rajeev Rhyth, Gowda Vamsi, Srinivas Roppy and Ahmed Helami Implementation of Sobel Edge Detection on DRBA and DIMArch Architectures	11:30:00	Owain Le Gonnelle, Miguel Chaurio, Anay Saha, Gonzalo Rosa and Fernando Pescador Energy Efficient Versatile Video Coding Decoder Using Lightweight Regression Models	11:30:00	Javier Soto, Sofía Vera, Yanel Fernández, Daniel Turge, Cecilia Hernández and Miguel Figueroa A sketch-based algorithm for network-flow entropy estimation on programmable switches using P4		
11:45:00		11:45:00	Hugues Almorin, Bertrand Le Gal, Christophe Jégo and Vincent Kissel Model based design of FMCW radar processing systems on FPGA platforms	11:45:00	Matthias Wess, Dominik Dallinger, Daniel Schödl, Matthias Bittner, Maximilian Göttinger and Axel Jantsch Energy Profiling of DNN Accelerators	11:45:00	Abdulrah Aljuffri, Mudi Saena, Cesar Rodolfo Weira Reibrecht, Sad Hamdoui and Mottajebah Touaf A Pre-Silicon Power Leakage Assessment Based on Generative Adversarial Networks		
12:00:00				12:00-13:00 Session 4 [Keynote] DSD 2 Wolfgang Ecker					
12:15:00									
12:30:00									
12:45:00									
13:00:00									
13:15:00									
13:30:00									
13:45:00									
14:00:00									
14:15:00									
14:30:00									
14:45:00	14:30-16:00 Session 5A Efficient Architecture for AI (Session Chair: Ocean Ozturk)	14:30:00	Mayank Kabra, Shryay V, Prashanth Hc, Kedhar Deshpande and Madhav Rao GCCell: A graph-search approach to design custom cells for computational subsystems	14:30:00	Ondrej Novak Deterministic Search Strategy of Compression Codes	14:30:00	Paolo Bellavista and Giuseppe Di Modica The IoTwins Methodology and Platform to Implement and Operate Digital Twins-based M.D. Applications in the Cloud Continuum		
15:00:00		15:00:00	Tomas Rabas, Jiri Bucek and Robert Lereš Single-Trace Attack on NTRU Decryption with Machine Learning and Template Profiling	15:00:00	Zaheer Tabassam and Andreas Steininger Towards Resilient Quasi-Delay Insensitive Conditional Control Elements	15:00:00	Altar Arrieta, Goliun Sagardui, Altar Agne, Wasif Afzal and Shaikat Ali DeVops for Cyber-Physical Systems: Objectives, Results and Lessons Learned from the Adeptness H2020 Project		
15:15:00		15:15:00	David Breuss, Maximilian Göttinger, Jenny Vuong, Clemens Reisinger and Axel Jantsch VADAR: A Vision-based Anomaly Detection Algorithm for Railroads	15:30:00	Nunzio Mirabella, Andrea Florida, Riccardo Cantoro, Michelangelo Grosso and Matteo Sonza Reorda Targeting different defect-oriented fault models in IC testing: an experimental approach	15:30:00	Rida Nouacer and Mahmoud Hussein COMP-DRONES: Contributions for Enabling Safe and Autonomous Drones		
15:30:00		15:30:00	Yamika Toza Diaz, Nicolás Landeros Muñoz, Rubén Gran-Tegre and Alejandro Valero On Fault-Tolerant Microarchitectural Techniques for Voltage Underscaling in On-Chip Memories of CNN Accelerators						
15:45:00		15:45:00	Archieb Michael Ezechi, Daniel Onwuchekwa and Roman Obermaier Optimization of the Versatile Tensor Accelerator (VTA) Load Module in a Time-Triggered Memory Access						
16:00:00									
16:15:00									
16:30:00									
16:45:00	16:30-18:00 Session 6A FPGA & AI (Session Chair: Eugenio Villar)	16:30:00	Cheol-Ho Choi and Hyun Woo Oh Disparity Refinement Processor Architecture utilizing Horizontal and Vertical Characteristics for Stereo Vision Systems	16:30:00	Jiliano Pimentel, Albtair A. McEwan and Hong Qing Yu A Novel Real-Time Framework for Embedded Systems Health Monitoring	16:30:00	Marko Anđelić, Junhao Chen, Riswan Tariq Syed, Fabian Vargas, Markus Ultsch, Miles Krutz, Stefan Bic, Milos Marjanovic, Sandra Veljkovic, Nikola Mitrovic, Danijel Dankovic, Goran Ristic, Radosław Duane, Nikola Vasevic, Aleksandar Jaksic, Alberto Palma, Antonio Lallena and Miguel Carvajal Towards a Smart Multi-Sensor Ionizing Radiation Monitoring System		
17:00:00		17:00:00	Mounika Vaddeboina, Endri Kaja, Alper Yilmazer, Sebastian Prebeck and Wolfgang Ecker Parallel Golomb-Rice Decoder with 8-bit Unary decoding for Weight Compression in TinyML Applications	17:00:00	Clément Gainé, Pierre-Alain Moellic, Olivier Potes and Jean-Max Duferme Fault Injection on Embedded Neural Networks: Impact of a Single Instruction Skip	17:00:00	Domenico Ragusa, Antonio J. Rodriguez Almeida, Stephan Nolting, Emanuele Torti, Hinar Fabelo, Ingo Hoyer, Alexander Utz and Gustavo M. Callico Acceleration of a CNN-based Heart Sound Segmenter: Implementation on Different Platforms Targeting a Wearable Device		
17:15:00		17:30:00	Ahmad Al-Zoubi, Benedikt Schable, Gianluca Martino and Goerschwin Fey Latency-optimized Hardware Acceleration of Multilayer Perceptron Inference	17:30:00	Daniel Onwuchekwa, Devika Joshi, Krishi Saha, Roman Obermaier and Tobias Pieper Fault-tolerant Lightweight High Level Architecture	17:30:00	Ramon Cana, Cristiano Chenet, Angeles Arbalaz, José-Maria Arsuaga, Josep Ll. Berzal, Aaron Call, Stefano Di Carlo, Juan José Costa, Dimitris Giropoulos, Vasileios Karakostas, Francisco Lubeiro, Konstantinos Pournikifidis, Dionisis Pournikifidis, Daniel Rubio, Alvia Rigo, Eva Rodriguez, Alessandro Savino, Alberto Sciotti, Nikolaos Tampouratzis and Alex Tomregosa Vitamin-V: Virtual Environment and Tool-boxing for Trustworthy Development of RISC-V based Cloud Services		
17:30:00		17:45:00	Kasper Hesse, Martin Schoeberl, Niels Age and Erik Triff On the Feasibility of using FPGA's for Efficient Topology Optimization	17:45:00	Leandro Lanzieri, Peter Kietzmann, Goerschwin Fey, Holger Schlab and Thomas C. Schmidt Aging Analysis of Embedded SRAM on a Large-Scale Testbed Using Machine Learning				
17:45:00									
Coffee Break and Poster Session P1									
ROOM 2									
16:30	16:30-18:00 Session 7A ASHWPA Advanced Systems in Healthcare, Wellness and Personal Assistance (Session Chair: Radovan Stojanovic)	16:30:00	Claire Béranger, Alexandre Bordat, Mohamed Amine Khelif, Petr Dobšid, Ngoc-Son Vu, Julien Le Kerne, David Guyard and Olivier Roman Radar-based Human Activity Acquisition, Classification and Recognition towards Elderly Fall Prediction	16:30:00	Kasper Hesse, Tjark Petersen and Jens Spars Asynchronous circuit design in Chisel using phase-decoupled Click Elements	16:30:00	Dina Hesse, Mabel Gay, Ila Pollan, ERF Bilge Kaun, Owen Millwood and Wilfrid Bartsch A Modular Open-Source Cryptographic Co-Processor for Internet of Things		
17:00		17:00:00	Antonio José Rodríguez Almeida, Hinar Fabelo Gómez, Cristina Sogero Ruiz, Rosa María Sánchez Hernández, Ana María Wagner and Gustavo Marrero Callico Novel Approach for AI-based Risk Calculator Development using Transfer Learning Targeting an Embedded System Implementation	17:00:00	Jure Vreča and Anton Blazizco Towards Deploying Highly Quantized Neural Networks on FPGA Using Chisel	17:00:00	Vincent Dumoulin, Natasha Devoye and Wenjing Rao Active learning for fast and slow modeling attacks on Arbitrator PUFs		
17:30		17:30:00	Emanuele Torti, Marco Gazzoni, Elisa Marelli, Razeel Leon, Gustavo Marrero Callico and Giovanni Danese An Attention-Based Parallel Algorithm for Hyperspectral Skin Cancer Classification on Low-Power GPUs	17:30:00	Erling Jellum, Yaman Umuruglu, Milica Orlandic and Martin Schoeberl Fpga-Isobits: Rapid Prototyping of FPGA Accelerators in Chisel	17:30:00	François Bonnal, Jean-Max Dutertre, Vincent Dupuquis and Olivier Potin Software-only Control-Flow Integrity against Fault Injection Attacks		
17:45:00		17:45:00	Angelo Corsaro, Luca Comandri, Olivier Hecart, Gabriele Baldoni, Julien Enoch, Pierre Autal, Julien Loudet, Carlos Guimaraes, Michael Bala and Damián Zenoh: Unifying Communication, Storage and Computation from the Cloud to the Microcontroller			17:45:00	Et, Laurent Lemarchand, David Espes, Fr Characterizing Intrusion Detection Systems On Heterogeneous Embedded Platforms		
ROOM 4									
ROOM 6									

In parallel with sessions 7

In parallel with sessions 6

Thursday 7th Sept 2023

Time	Room 1	ROOM 3	ROOM 5									
08:30:00												
08:45:00												
09:00:00	09:00-10:00 Session 8 [Keynote] SEAA1											
09:15:00												
09:30:00												
09:45:00												
10:00:00												
10:15:00	10:00-10:30 Coffee Break & Session 8 Poster Session P2											
10:30:00	10:30-12:00 Session 9A : Micro- architecture (Session Chair Benoit Dinechin)	10:30:00	Yakup Hüner and Ramazan Yeniçeri	ComCoS: Enhanced Cache Partitioning Technique for Integrated Modular Avionics	10:30-12:00 Session 9B NoC and Low Level Design (Session Chair : Lilla Zaourar)	10:30:00	Denis Shemonae, Bertrand Le Gal, Christophe Jego and Anthony Besseau	Implementation of an Assignment Algorithm for Object Tracking on a FPGA MPSoC	10:30-12:00 Session 9C EPDSD-3 (Session Chair: )	10:30:00	Irene Agirre, Alejandro J. Calderon, Irune Yarza, Imanol Mugarza, David Garcia, Lucas Borracci, Patrick Uven and Alvaro Jover	UP2DATE software updating framework compliance with safety and security regulations and standards
10:45:00		11:00:00	Marcelo Ruaro, Hadrien Barral, Matteo Bertolino, Rodrigo Cataldo, Roberto Medina, Etienne Borde and Mohamed Karaoui	The Last-Level-Cache Interference in Guest Performance: a Case-Study with Zephyr OS		11:00:00	Arman Ferdowsi, Matthias Fuegger, Josef Salzmann and Ulrich Schmid	A Hybrid Delay Model for Interconnected Multi-Input Gates		11:00:00	Yubal Barrios, Francisco Sanjuán, Geoffroy Bordot, Hella Sharif, Jerome Bernier and Sebastian Lopez	Demonstrator development of a next-generation video instrument for Earth Observation
11:00:00		11:30:00	Burak Ocalan and Ozcan Ozturk	Utilizing Prefetch Buffers for Iterative Graph Applications		11:30:00	Simon Friedrich, Chia-Ying Lin, Viktor Razilov, Robert Wittig, Emil Matus and Gerhard Fettweis	Access Interval Prediction with Neural Networks for Tightly Coupled Memory Systems		11:30:00	Carlo Centofanti, Claudia Rinaldi, Andrea Marotta, Christos Verikoukis, Nikos Passas, Dionysis Xenakis, Stefano Tennina and Dajana Cassioli	OPTIMIST: OPTIMised video content delivery chains over joint multi-access edge computing and 5G radio network infrastructures
11:15:00		11:45:00	Xabier Arauzo, Irune Yarza, Leonidas Kosmidis, Alejandro Calderón and Marcos Rodríguez	Unraveling the Mystery of NVIDIA's UM for SafetyCritical GPU systems								
11:30:00												
11:45:00												
12:00:00	12:00-13:00 Session 10 [Keynote] DSD 3 Maciej Ciesielski											
12:15:00	13:00-14:30 Lunch Break											
12:30:00												
12:45:00												
13:00:00												
13:15:00												
13:30:00	DSD 2023 and SEAA 2023 Social life											
13:45:00												
14:00:00												
14:15:00												
14:30:00												
14:45:00												
15:00:00												
15:15:00												
15:30:00												
15:45:00												
16:00:00												
16:15:00												
16:30:00												
16:45:00												
17:00:00												
17:15:00												
17:30:00												
17:45:00												
18:00:00												
18:15:00												
18:30:00												
18:45:00												
19:00:00												
19:15:00												
19:30:00												

\* Papers in red are "Best paper award candidates"

Friday 8th Sept 2023

Time	ROOM 1	ROOM 3	ROOM 5	
09:00:00				
09:15:00				
09:30:00	09:00-10:00 Session 11 [Keynote] SEAA2			
09:45:00				
10:00:00	10:00-10:30 Coffee Break & Poster Session P3			
10:15:00				
10:30:00	Thur 10:30-12:00 Fri 15:00 Session 12A: HSTEC-1 Hardware, Software, and Tools for the IoT-to-Edge-to-Cloud Continuum (Session Chair: Daniel Casini)	10:30:00 Gonzalo Rosa Olmeda, Cristina Sánchez Carabias, Victoria Cunha Añes, Manuel Villa Romero, Alberto Martín-Pérez, Miguel Chavarrias, Alfonso Lagares, Eduardo Juárez and César Sanz Transmittance hyperspectral capture system and methodology assessment for blood-liquid serum samples analysis	10:30:00 Domink Marchanteller and Johanna Sepúlveda A PQC and QKD Hybridization for Quantum-Secure Communications	
10:45:00		10:30:00 Raquel Leon, Himar Fabelo, Samuel Ortega, Juan F. Piñeiro, Adam Szolna, Jesus Morera, Bernardino Clavo and Gustavo Marrero Callico Evaluation of Hyperspectral Imaging Fusion for in-vivo Brain Tumor Identification and Delineation		
11:00:00		11:00:00 Beatriz Martínez-Vega, Raquel Leon, Himar Fabelo, Samuel Ortega, Eduardo Quevedo, Angeles Canovas-Molina, Francisco Rodriguez-Esparagon, Bernardino Clavo and Gustavo M. Callico Analysis of the behavior of Ozone Therapy in Chemotherapy-induced Neuropathy using Hyperspectral Imaging Technology	10:30-12:00 Session 12C SPCPS-1 (Session Chair: Camelia Silman)	11:00:00 Filiret Basic, Christian Seifert, and Robert Kofler Secure Data Acquisition for Battery Management Systems
11:15:00		11:00:00 Gianmarco Ottavi, Florian Zaruba, Luca Benini and Davide Rossi Reducing Load-Use dependency-induced performance penalty in the Open-Source RISC-V CVAs CPU	11:15:00 Alejandro Martínez de Terro, Jaime Sancho, Alberto Martín-Pérez, Manuel Villa, Guillermo Vázquez, Pedro L. Cebrían, Gonzalo Rosa Olmeda, Pallab Sutradhar, Miguel Chavarrias, Eduardo Juárez and Cesar Sanz Real-time hyperspectral and depth fusion calibration method for improved reflectance measures on arbitrary complex surfaces	
11:30:00		11:30:00 Pietro Fara, Gabriele Serra and Federico Aromolo Bounded transmission latency in real-time edge computing: a scheduling analysis	11:30:00 Daniel Fernandez, Carlos González and Daniel Mozos Real-time Independent Components Analysis for Dimensional Reduction of Hyperspectral Images Using Reconfigurable Hardware	11:30:00 Matthias Stammler, Matthias Hamann, Tanja Harbaum and Juergen Becker Mitigating Masking in Automotive Communication Systems: Modelling and Hardware Generation
11:45:00			11:45:00 Raquel Leon, Himar Fabelo, Samuel Ortega, Juan F. Piñeiro, Adam Szolna, Jesus Morera, Bernardino Clavo and Gustavo Marrero Callico Evaluation of Hyperspectral Imaging Fusion for in-vivo Brain Tumor Identification and Delineation	
12:00:00	12:00-13:30 Lunch Break			
12:15:00				
12:30:00				
12:45:00				
13:00:00				
13:15:00				
13:30:00	13:30-15:00 Session 13A DSD & Applications-1 (Session Chair Hamza Ouarnoughi)	13:30:00 Fabian Kreß, Alexey Serdyuk, Micha Hiegle, Disnebio Waldmann, Tim Hoffbiter, Julian Höfer, Tim Hamann, Jens Barth, Peter Klämpf, Tanja Harbaum and Juergen Becker ATLAS: An Approximate Time-Series LSTM Accelerator for Low-Power IoT Applications	13:30:00 Gabriele Serra, Pietro Fara and Daniel Casini Enhancing the Availability of Web Services in the IoT-to-Edge-to-Cloud Compute Continuum: A WordPress Case Study	13:30:00 Richard Růžička, Václav Šimek and Jan Nevoral Polymorphic RTL Computational Elements
13:45:00		14:00:00 Verónica Isandar, Mohamed A. Abd El Ghany and Diana Goehringler Auto-DOK: Compiler-Assisted Automatic Detection of Offload Kernels for FPGA-HBM Architectures	14:00:00 Jose Antonio De La Torre Las Heras, Fernando Rincón, Marco Zennaro, Julian Caba, Jesús Barba and Juan Carlos López SimIoT: A simulator for verification and profiling of complex IoT deployments	14:00:00 Yujl Wada and Shigeru Yamashita Minimizing the Impact of Unbalanced Splitting Errors on DMFBs Without Any Overhead
14:00:00		14:30:00 Najdet Charaf, Julian Haase, Adrian Kullsch, Christian von Elm, Nico Volkens and Diana Goehringler RTASS: a RunTime Adaptable and Scalable System for Network-on-Chip-Based Architectures	14:30:00 Florian Schade, Tobias Dörr, Alexander Ahlbrecht, Vincent Janson, Umut Durak and Juergen Becker Automatic Deployment of Embedded Real-time Software Systems to Hypervisor-managed Platforms	14:30:00 Gerold Fink, Florina Costamoling, Philipp Ebner and Robert Wille Efficient Simulation of Droplet Merging in Channel-based Microfluidic Devices
14:15:00		14:45:00 Guillaume Ollier, Morayo Adedjouma, Simos Gerasi mou and Chokri Mraidha An Ontological Approach for Dependability Analysis of Automated Systems		
14:30:00				
14:45:00				
15:00:00	Coffee Break 15:00-15:15			
15:15:00	15:15-16:45 Session 14A OS & Real-Time Systems (Session Chair: Eugenio Villar)	15:15:00 Mehmet Şirin Önen and Arda Yurdakul Container Scheduling Under ARINC 653 Scheduler Constraints	15:15:00 Mehrdad Poorhosseini and Kim Gruettner A RISC-V based platform supporting mixed timing-critical and high performance workloads	15:15:00 Mojtaba Mahdavi A Novel Memristive-Based Data Reordering Scheme
15:45:00		15:45:00 Lukas Miedema and Clemens Greck Change of plans: optimizing for power, reliability and timeliness for cost-conscious real-time systems	15:45:00 Hyun Woo Oh, Cheol-Ho Choi, Jeong Woo Cha, Hyunmin Choi, Joon Hwan Han and Jung-Ho Shin An SoC FPGA-based Integrated Real-time Image Processor for Uncooled Infrared Focal Plane Array	15:45:00 Muhammad Ali Siddiq, Jan Andrés Galvan Hernández, Antehneh Gebregiorgis, Rajendra Bishnoi, Christos Stylian, Saif Hamdoul and Mottaqiallah Taouil Memristor-Based Lightweight Encryption
16:00:00		16:00:00 Antti Nurmi, Per Lindgren and Tom Szymkowiak AntIQ: A Hardware-Accelerated Priority Queue Design with Constant Time Arbitrary-Element Removal	16:15:00 Viktor Teren, Jordi Cortadella and Tiziano Villa Seto: a framework for the decomposition of Petri nets and transition systems	16:15:00 Orégane Desreuntes, Benoît Dupont De Dinechin and Julien Le Maire Exact Dot-Product Accumulate Operators for 8-bit Floating-Point Deep Learning
16:15:00		16:15:00 Vinay Rayapati, Mahati Basawaraja and Madhav Rao High Performance and Energy Efficient AMD and BWAD Pooling Schemes Characterised for CNN Accelerators	16:30:00 Raphael Schermann, Rainer Urian and Christian Steger Integration of the TPM In the AACKA protocol	
16:30:00		16:30:00 Peter Hobden, Saket Srivastava and Edmond Nurellari FPGA based 77GHz RADAR processing with novel linearisation		
16:45:00	Closing Session and Best Paper Award Nomination			

\* Papers in red are "Best paper award candidates"

**POSTERS**

<b>POSTERS</b>			
<b>Poster Sessions P1</b>		<b>Wednesday 6th Sept 16:00-16:15</b>	
<b>AAMTM, AHSa</b>			
8655	Weiyang Zhang, Mehran Goli, Muhammad Hassan and Rolf Drechsler	Approach across Different Microarchitectures for RISC-V Processors	Poster
9066	Daniel Schnoell, Matthias Wess, Matthias Bittner, Maximilian Götzinger and Axel Jantsch	Fast, Quantization Aware DNN Training for Efficient HW Implementation	Poster
6078	Marcin Aftowicz, Ievgen Kabin, Zoya Dyka and Peter Langendoerfer	Non-profiled semi-supervised horizontal attack against Elliptic Curve Scalar Multiplication using Support Vector Machines	Poster
9234	Marios Papadopoulos and Paris Kitsos	FPGA-based Encryption System for Cloud Security	Poster
3593	<u>Isabella Piacentini, Alessandro Barengi and Gerardo Pelosi</u>	A Computation Interleaving Countermeasure against Profiled Side Channel Attacks	Poster
<b>Poster Session P2</b>		<b>Thursday 07th Sept 10:00-10:30</b>	
<b>DSD</b>			
9101	Cornelia Wulf, Julian Haase, Matthias Nickel and Diana Goehringer	Virtualization of Hardware Accelerators in a Network-on-Chip	Poster
7292	Meinhard Kissich and Marcel Baunach	of Functional Flaws in Digital Designs: A Designer's Guide	Poster
7554	Vibhor Jain, Sajid Mohamed, Dip Goswami and Sander Stuijk	Vision-based multi-size object positioning	Poster
8698	Ensieh Aliagha, Veronia Iskandar, Stephan Enseleit and Diana Göhringer	Investigating the Impact of Non-Volatile Memories on Energy-Efficiency of Coarse-Grained Reconfigurable Architectures	Poster
5797	Lilia Zaourar, Alice Chillet and Jean-Marc Philippe	A-DECA : an Automated Design space Exploration approach for Computing Architectures to develop efficient high-performance many core processors	Poster
<b>Poster Session 3</b>		<b>Friday 08th Sept 10:00-10:30</b>	
<b>HIAA, FTET, DDVC</b>			
1067	Mario Guanche-Hernández, Pedro P. Carballo and Raquel Leon	MPSoC FPGA Implementation of Algorithms of Machine Learning for Clinical Applications Using High-Level Design Methodology	Poster
3081	Philipp Ebner and Robert Wille	CFD for Microfluidics: A Workflow for Setting Up the Simulation of Microfluidic Devices	Poster
4304	Anna Bernasconi, Valentina Ciriani, Asma Taheri Monfared and Stefano Zanoni	Compact Quantum Circuits for Dimension Reducible Functions	Poster
7048	Kasper Hesse	Implementation and Verification of the Argo Network-on-Chip in Chisel	Poster
7472	Muhammad Sohail Ibrahim, Muhammad Usman, Malik Zohaib Nisar and Jeong-A Lee	DSLOT-NN: Digit-Serial Left-to-Right Neural Network Accelerator	Poster

#	Authors	Title	Decision	Track
<b>AAMTM</b>				
5231	Owen Le Gonidec, Miquel Chavarrias, Anup Saha,	Energy Efficient Versatile Video Coding Decoder Using Lightweight	Short Presentation	AAMTM
6108	Matthias Wess, Dominik Dallinger, Daniel Schnoll,	Energy Profiles of DNN Accelerators	Short Presentation	AAMTM
8323	Dominika Przewlocka-Rus and Tomasz Kryjak	Power-of-Two Quantized YOLO Network for Pedestrian Detection with	Long Presentation	AAMTM
7472	Muhammad Sohail Ibrahim, Muhammad Usman, Malik	DSL OT-NN: Dual-Serial Left-to-Right Neural Network Accelerator	Poster	AAMTM
8655	Weiyang Zhang, Mehran Goli, Muhammad Hassan and	Efficient ML-Based Performance Estimation Approach across Different	Poster	AAMTM
9066	Daniel Schoell, Matthias Wess, Matthias Bitner,	Fast Quantization Aware DNN Training for Efficient HW	Poster	AAMTM
<b>AHSA</b>				
3648	Nico Mexis, Tolga Arul, Nikolaos Athanasios	Spatial Correlation in Weak Physical Unclonable Functions: A	Long Presentation	AHSA
3743	Dina Hesse, Mael Gay, Ila Polian, Eilf Bilge Kavun,	A Modular Open-Source Cryptographic Co-Processor for Internet of	Long Presentation	AHSA
8669	Nikolaos Athanasios Anagnostopoulos, Nico Mexis,	A Method to Construct Efficient Carbon-Nanotube-Based Physical	Long Presentation	AHSA
9747	Clément Gaine, Pierre-Alain Moellic, Olivier Potin and	Fault Injection on Embedded Neural Networks: Impact of a Single	Long Presentation	AHSA
6078	Marcin Aftowicz, Iwgen Kabin, Zoya Dyka and Peter	Non-profiled semi-supervised horizontal attack against Elliptic Curve	Poster	AHSA
9234	Marinos Papadopoulos and Paris Kilsos	FPGA-based Encryption System for Cloud Security	Poster	AHSA
3593	Isabella Piacentini, Alessandro Barenghi and Gerardo	A Computation Interleaving Countermeasure against Profiled Side	Poster	AHSA
<b>ASHWPA</b>				
919	Emanuele Torti, Marco Gazzoni, Elisa Marenzi, Raquel	An Attention-Based Parallel Algorithm for Hyperspectral Skin Cancer	Short Presentation	ASHWPA
4151	Claire Béranger, Alexandre Bordat, Mohamed Amine	Radar-based Human Activity Acquisition, Classification and	Long Presentation	ASHWPA
5514	Antonio José Rodríguez Almeida, Himar Fabelo Gómez,	Novel Approach for AI-based Risk Calculator Development using	Long Presentation	ASHWPA
<b>DDVC</b>				
1009	Erlina Jellum, Yaman Umuruglu, Milica Orlandic and	fpga-tidbits: Rapid Prototyping of FPGA Accelerators in Chisel	Long Presentation	DDVC
1840	Hans Jakob Damsgaard, Aleksandr Ometov and Jari	Generating CGRA Processing Element Hardware with CGRAgen	Long Presentation	DDVC
3533	Kasper Hesse, Tark Petersen and Jens Sparse	Asynchronous circuit design in Chisel using phase-decoupled Click	Long Presentation	DDVC
8532	Jure Vreca and Antonio Blasizzo	Towards Deploying Highly Quantized Neural Networks on FPGA	Long Presentation	DDVC
7048	Kasper Hesse	Implementation and Verification of the Argo Network-on-Chip in	Poster	DDVC
<b>DSD main track</b>				
3119	Cheol-Ho Choi and Hyun Woo Oh	Disparity Refinement Processor Architecture utilizing Horizontal and	Long Presentation	DSD'2023
538	Fabian Krefß, Alexey Serdyuk, Micha Hiegle, Disnebio	ATLAS: An Approximate Time-Series LSTM Accelerator for Low-	Long Presentation	DSD'2023
	Waldmann, Tim Hotfliter, Julian Höfer, Tim Hamann, Jens	Power IoT Applications		
	Barth, Peter Kämpf, Tanja Harbaum and Juergen Becker			
3452	Mounika Vaddeboina, Endri Kaja, Alper Yilmazer,	Parallel Golomb-Rice Decoder with 8-bit Unary decoding for Weight	Long Presentation	DSD'2023
	Sebastian Prebeck and Wolfgang Ecker	Compression in TinyML Applications		
2762	Hyun Woo Oh, Cheol-Ho Choi, Jeong Woo Cha,	An SoC FPGA-based Integrated Real-time Image Processor for	Long Presentation	DSD'2023
	Hyunmin Choi, Joon Hwan Han and Jung-Ho Shin	Uncooled Infrared Focal Plane Array		
5469	Veronia Iskandar, Mohamed A. Abd El Ghany and Diana	Auto-DOK: Compiler-Assisted Automatic Detection of Offload Kernels	Long Presentation	DSD'2023
	Goehringer	for FPGA-HBM Architectures		
1536	Agathe Archet, Nicolas Ventroux, Nicolas Gac and	Energy-efficient use of an embedded heterogeneous SoC for the	Long Presentation	DSD'2023
	Francois Orioux	inference of CNNs		
5832	Marcelo Ruaro, Hadrien Barral, Matteo Bertolino,	The Last-Level-Cache Interference in Guest Performance: a Case-	Long Presentation	DSD'2023
	Rodrigo Cataldo, Roberto Medina, Etienne Borde and	Study with Zephyr OS		
	Mohamed Karraoui			
6480	Yakup Hüner and Ramazan Yeniçeri	ComCoS: Enhanced Cache Partitioning Technique for Integrated	Long Presentation	DSD'2023
		Modular Avionics		
6786	Simon Friedrich, Chia-Ying Lin, Viktor Razilov, Robert	Access Interval Prediction with Neural Networks for Tightly Coupled	Long Presentation	DSD'2023
	Wittig, Emil Matus and Gerhard Fettweis	Memory Systems		
5904	Arman Ferdowsi, Matthias Fuegger, Josef Salzmann and	A Hybrid Delay Model for Interconnected Multi-Input Gates	Long Presentation	DSD'2023
	Ulrich Schmid			
5162	Mayank Kabra, Shreyas Vs, Prashanth Hc, Kedar	GCCells: A graph-search approach to design custom cells for	Long Presentation	DSD'2023
	Deshpande and Madhav Rao	computational subsystems		
9130	Denis Shmonaev, Bertrand Le Gal, Christophe Jégo	Implementation of an Assignment Algorithm for Object Tracking on a	Long Presentation	DSD'2023
	and Anthony Besseau	FPGA MPSoC		
7239	Najdet Charaf, Julian Haase, Adrian Kulisch, Christian	RTASS: a RunTime Adaptable and Scalable System for Network-on-	Long Presentation	DSD'2023
	von Elm, Nico Volkens and Diana Goehringer	Chip-Based Architectures		
6888	Stefano Marti, Enis Mustafa, Giacomo Bisson, Prayush	FPGA-based real-time laser beam profiling and stabilization system	Long Presentation	DSD'2023
	Anand, Philipp Fabritius, Tilman Esslinger and	for quantum simulation applications		
	Abdulkadir Akin			
2553	Muhammad Ali Siddiqi, Jan Andrés Galvan Hernández,	Memristor-Based Lightweight Encryption	Long Presentation	DSD'2023
	Anteneh Gebregiorgis, Rajendra Bishnoi, Christos			
	Strydis, Said Hamdioui and Mottaqiallah Taouil			
2384	Florian Schade, Tobias Dörr, Alexander Ahlbrecht,	Automatic Deployment of Embedded Real-time Software Systems to	Long Presentation	DSD'2023
	Vincent Janson, Umut Durak and Juergen Becker	Hypervisor-managed Platforms		
2758	Mehmet Şirin Onen and Arda Yurdakul	Container Scheduling Under ARINC 653 Scheduler Constraints	Long Presentation	DSD'2023
6594	Maciej Ciesielski	Formal Methods in Arithmetic Circuit Verification: a Brief History and	Long Presentation	DSD'2023
		Challenges		
1591	Vincent Dumoulin, Natasha Devroye and Wenjing Rao	Active learning for fast and slow modeling attacks on Arbiter PUFs	Long Presentation	DSD'2023
9101	Cornelia Wulf, Julian Haase, Matthias Nickel and Diana	Virtualization of Hardware Accelerators in a Network-on-Chip	Poster	DSD'2023
	Goehringer			
7292	Meinhard Kissich and Marcel Baunach	Formal Property Verification for Early Discovery of Functional Flaws in	Poster	DSD'2023
		Digital Designs: A Designer's Guide		
7554	Vibhor Jain, Sajid Mohamed, Dip Goswami and Sander	Vision-based multi-size object positioning	Poster	DSD'2023
	Stuijk			
8698	Ensieh Aliaqha, Veronia Iskandar, Stephan Enseleit and	Investigating the Impact of Non-Volatile Memories on Energy-	Poster	DSD'2023
	Diana Gohringer	Efficiency of Coarse-Grained Reconfigurable Architectures		
5797	Lilia Zaourar, Alice Chillet and Jean-Marc Philippe	A-DECA: an Automated Design space Exploration approach for	Poster	DSD'2023
		Computing Architectures to develop efficient high-performance many		
		core processors		
9639	Javier Solo, Sofia Vera, Yaimé Fernández, Daniel Yunge,	A sketch-based algorithm for network-flow entropy estimation on	Short Presentation	DSD'2023
	Cecilia Hernández and Miguel Figueroa	programmable switches using P4		
8491	Orégane Desrentes, Benoit Dupont De Dinechin and	Exact Dot-Product Accumulate Operators for 8-bit Floating-Point Deep	Short Presentation	DSD'2023
	Julien Le Maire	Learning		
3224	Pudi Dhilleswararao, Rajeev Ryansh, Gouda Vamsi,	Implementation of Sobel Edge Detection on DRRA and DiMArch	Short Presentation	DSD'2023
	Srinivas Boppu and Ahmed Hemani	Architectures		
8480	David Breuss, Maximilian Götzinger, Jenny Vuong,	VADAR: A Vision-based Anomaly Detection Algorithm for Railroads	Short Presentation	DSD'2023
	Clemens Reinsner and Axel Jantsch			
7966	Vinay Rayapati, Mahat Basavaraju and Madhav Rao	High Performance and Energy Efficient AMD and BWAD Pooling	Short Presentation	DSD'2023
		Schemes Characterised for CNN Accelerators		
501	Kasper Hesse, Martin Schoeberl, Niels Aage and Erik	On the Feasibility of using FPGA's for Efficient Topology Optimization	Short Presentation	DSD'2023
	Träff			
2376	Peter Hobden, Saket Srivastava and Edmond Nurellari	FPGA based 77GHz RADAR processing with novel linearisation	Short Presentation	DSD'2023
9297	Hugues Almorin, Bertrand Le Gal, Christophe Jégo and	Model based design of FMCW radar processing systems on FPGA	Short Presentation	DSD'2023
	Vincent Kissel	platforms		
932	Yamilka Toca-Díaz, Nicolás Landeros Muñoz, Rubén	On Fault-Tolerant Microarchitectural Techniques for Voltage	Short Presentation	DSD'2023
	Gran-Tejero and Alejandro Valero	Underscaling in On-Chip Memories of CNN Accelerators		
5519	Ahmad Al-Zoubi, Benedikt Schabibe, Gianluca Martino	Lalency-optimized Hardware Acceleration of Multilayer Perceptron	Short Presentation	DSD'2023
	and Goerschwinn Fey	Inference		
6215	Aniebiel Micheal Ezekiel, Daniel Onwuchekwa and	Optimization of the Versatile Tensor Accelerator (VTA) Load Module	Short Presentation	DSD'2023
	Roman Obermaisser	in a Time-Triggered Memory Access		
3211	Anti Nurni, Per Lindgren and Tom Szymkowiak	AntiQ: A Hardware-Accelerated Priority Queue Design with Constant	Short Presentation	DSD'2023
		Time Arbitrary-Element Removal		
6606	Burak Ocalan and Ozcan Ozturk	Utilizing Prefetch Buffers for Iterative Graph Applications	Short Presentation	DSD'2023
3202	Lukas Miedema and Clemens Grellck	Change of plans: optimizing for power, reliability and timeliness for	Short Presentation	DSD'2023
		cost-conscious real-time systems		
2138	Xabier Arauzo, Irune Yarza, Leonidas Kosmidis,	Unraveling the Mystery of NVIDIA's UM for SafetyCritical GPU systems	Short Presentation	DSD'2023
	Alejandro Calderón and Marcos Rodríguez			
3217	Viktor Teren, Jordi Cortadella and Tiziano Villa	Seto: a framework for the decomposition of Petri nets and transition	Short Presentation	DSD'2023
		systems		
3288	François Bonnal, Jean-Max Duertre, Vincent Dupaquis	Software-only Control-Flow Integrity against Fault Injection Attacks	Short Presentation	DSD'2023
	and Olivier Potin			

3967	Guillaume Ollier, Morayo Adedjouma, Simos Gerasimou and Chokri Mraidha	An Ontological Approach for Dependability Analysis of Automated Systems	Short Presentation	DSD'2023
3525	Daniel Onwuchekwa, Devika Joshi, Krishi Savla, Roman Obermaier and Tobias Pieper	Fault-tolerant Lightweight High Level Architecture	Short Presentation	DSD'2023
320	Camelia Silimani, Louis Morge-Rollet, Laurent Lemarchand, David Espes, Frédéric Le Roy and Jaill Boukhobza	Characterizing Intrusion Detection Systems On Heterogeneous Embedded Platforms	Short Presentation	DSD'2023
4620	Tomas Rabas, Jiri Bucek and Róbert Lórencz	Single-Trace Attack on NTRU Decryption with Machine Learning and Template Profiling	Short Presentation	DSD'2023
7591	Abdullah Aljiffri, Mudit Saxena, Cezar Rodolfo Wedig Reinbrecht, Said Hamdioui and Mottaqiallah Taouil	A Pre-Silicon Power Leakage Assessment Based on Generative Adversarial Networks	Short Presentation	DSD'2023
<b>DTFT</b>				
516	Ondrej Novak	Deterministic Search Strategy of Compression Codes	Long Presentation	DTFT
3541	Zaheer Tabassam and Andreas Steininger	Towards Resilient Quasi Delay Insensitive Conditional Control Elements	Long Presentation	DTFT
4196	Nunzio Mirabella, Andrea Floridia, Riccardo Cantoro, Michelangelo Grosso and Matteo Sonza Reorda	Targeting different defect-oriented fault models in IC testing: an experimental approach	Long Presentation	DTFT
4488	Juliano Pimentel, Alistair A. McEwan and Hong Qing Yu	A Novel Real-Time Framework for Embedded Systems Health Monitoring	Long Presentation	DTFT
9001	Leandro Lanzieri, Peter Kietzmann, Goerschwim Fey, Holger Schlarb and Thomas C. Schmidt	Ageing Analysis of Embedded SRAM on a Large-Scale Testbed Using Machine Learning	Short Presentation	DTFT
<b>EPDSD</b>				
4229	Paolo Bellavista and Giuseppe Di Modica	The IoTwins Methodology and Platform to Implement and Operate Digital Twins-based 4.0 Applications in the Cloud Continuum	INVITED	EPDSD
4636	Aitor Arrieta, Gouriia Sagardui, Aitor Agirre, Wasif Afzal and Shaikat Ali	DevOps for Cyber-Physical Systems: Objectives, Results and Lessons Learned from the Adeptness H2020 Project	INVITED	EPDSD
5481	Reda Nouacer and Mahmoud Hussein	COMP4DRONES Contributions for Enabling Safe and Autonomous Drones	INVITED	EPDSD
6468	Marko Andjelkovic, Junchao Chen, Rizwan Tariq Syed, Fabian Vargas, Markus Ulbricht, Milos Krstic, Stefan Ilic, Milos Marjanovic, Sandra Veljkovic, Nikola Mitrovic, Danijel Dankovic, Goran Ristic, Russell Duane, Nikola Vasovic, Aleksandar Jaksic, Alberto Palma, Antonio Lallena and Miguel Carvajal	Towards a Smart Multi-Sensor Ionizing Radiation Monitoring System	INVITED	EPDSD
6916	Domenico Ragusa, Antonio J. Rodriguez Almeida, Stephan Nolting, Emanuele Torti, Himar Fabelo, Ingo Hoyer, Alexander Utz and Gustavo M. Callico	Acceleration of a CNN-based Heart Sound Segmenter: Implementation on Different Platforms Targeting a Wearable Device	INVITED	EPDSD
7268	Ramon Canal, Cristiano Chenet, Angelos Arelakis, José-Maria Arnaú, Josep L. Berral, Aaron Call, Stefano Di Carlo, Juan José Costa, Dimitris Gizopoulos, Vasileios Karakostas, Francesco Lubrano, Konstantinos Nikas, Yiannis Nikolakopoulos, Beatriz Otero, George Papadimitriou, Ioannis Papaefstathiou, Dionisios Pnevmatikatos, Daniel Raho, Alvisé Rigo, Eva Rodríguez, Alessandro Savino, Alberto Scionti, Nikolaos Tampouratzis and Alex Torregrosa	Vitamin-V: Virtual Environment and Tool-boxing for Trustworthy Development of RISC-V based Cloud Services	INVITED	EPDSD
8656	Irune Agirre, Alejandro J. Calderon, Irune Yarza, Imanol Mugarza, David Garcia, Lucas Borracci, Patrick Uven and Alvaro Jover	UP2DATE software updating framework compliance with safety and security regulations and standards	INVITED	EPDSD
8939	Yubal Barrios, Francisco Sanjuán, Geoffroy Bordot, Heila Sharif, Jerome Bernier and Sebastian Lopez	Demonstrator development of a next-generation video instrument for Earth Observation	INVITED	EPDSD
9055	Carlo Centofanti, Claudia Rinaldi, Andrea Marotta, Christos Verikoukis, Nikos Passas, Dionysios Xenakis, Stefano Tennina and Dajana Cassioli	OPTIMIST: OPTIMised video content delivery chains over joint multi-access edge computing and 5G radio network infrastructures	INVITED	EPDSD
<b>FTET</b>				
3346	Richard Růžička, Václav Simek and Jan Nevorál	Polymorphic RTL Computational Elements	Long Presentation	FTET
5108	Yuji Wada and Shigeru Yamashita	Minimizing the Impact of Unbalanced Splitting Errors on DMFBs Without Any Overhead	Long Presentation	FTET
5711	Gerold Fink, Florina Costamoling, Philipp Ebner and Robert Wille	Efficient Simulation of Droplet Merging in Channel-based Microfluidic Devices	Long Presentation	FTET
6277	Mojtaba Mahdavi	A Novel Memristive-Based Data Reordering Scheme	Long Presentation	FTET
3081	Philipp Ebner and Robert Wille	CFD for Microfluidics: A Workflow for Setting Up the Simulation of Microfluidic Devices	Poster	FTET
4304	Anna Bernasconi, Valentina Ciriani, Asma Taheri Monfared and Stefano Zanoni	Compact Quantum Circuits for Dimension Reducible Functions	Poster	FTET
<b>HIAAA</b>				
1644	Beatriz Martínez-Vega, Raquel Leon, Himar Fabelo, Samuel Ortega, Eduardo Quevedo, Angeles Canovas-Molina, Francisco Rodríguez-Esparragon, Bernardino Clavo and Gustavo M. Callico	Analysis of the behavior of Ozone Therapy in Chemotherapy-induced Neuropathy using Hyperspectral Imaging Technology	Long Presentation	HIAAA
2160	Alejandro Martínez de Temero, Jaime Sancho, Alberto Martín-Pérez, Manuel Villa, Guillermo Vázquez, Pedro L. Cebrián, Gonzalo Rosa Olmeda, Pallab Sutradhar, Miguel Chavarrias, Eduardo Juarez and Cesar Sanz	Real-time hyperspectral and depth fusion calibration method for improved reflectance measures on arbitrary complex surfaces	Long Presentation	HIAAA
6947	Gonzalo Rosa Olmeda, Cristina Sánchez Carabias, Victoria Cunha Alves, Manuel Villa Romero, Alberto Martín-Pérez, Miguel Chavarrias, Alfonso Lagares, Eduardo Juarez and César Sanz	Transmittance hyperspectral capture system and methodology assessment for blood-liquid serum samples analysis	Long Presentation	HIAAA
7722	Raquel Leon, Himar Fabelo, Samuel Ortega, Juan F. Piñeiro, Adam Szolna, Jesus Morera, Bernardino Clavo and Gustavo Marrero Callico	Evaluation of Hyperspectral Imaging Fusion for in-vivo Brain Tumor Identification and Delineation	Long Presentation	HIAAA
9318	Daniel Fernandez, Carlos González and Daniel Mozos	Real-time Independent Components Analysis for Dimensional Reduction of Hyperspectral Images Using Reconfigurable Hardware	Long Presentation	HIAAA
1067	Mario Guanche-Hernández, Pedro P. Carballo and Raquel Leon	MPSoC FPGA Implementation of Algorithms of Machine Learning for Clinical Applications Using High-Level Design Methodology	Poster	HIAAA
1261	Roger Perelló Gumbau, Christian Díaz De Arcos and Fahima Chowdhury	Exploration of a Methodology for Hyperspectral Band Selection using XGBoost and PCA	Poster	HIAAA
<b>HSTIEC</b>				
1039	Gabriele Serra, Pietro Fara and Daniel Casini	Enhancing the Availability of Web Services in the IoT-to-Edge-to-Cloud Compute Continuum: A WordPress Case Study	Long Presentation	HSTIEC
5298	Jose Antonio De La Torre Las Heras, Fernando Rincón, Marco Zennaro, Julian Caba, Jesús Barba and Juan Carlos López	SimIoT: A simulator for verification and profiling of complex IoT deployments	Long Presentation	HSTIEC
6475	Pietro Fara, Gabriele Serra and Federico Aromolo	Bounded transmission latency in real-time edge computing: a scheduling analysis	Long Presentation	HSTIEC
1766	Angelo Corsaro, Luca Cominardi, Olivier Hecart, Gabriele Baldoni, Julien Enoch, Pierre Avital, Julien Loudet, Carlos Guimares, Michael Ilyin and Dmitri Bannov	Zenoh: Unifying Communication, Storage and Computation from the Cloud to the Microcontroller	INVITED	HSTIEC
8062	Gianmarco Ottavi, Florian Zaruba, Luca Benini and Davide Rossi	Reducing Load-Use dependency-induced performance penalty in the Open-Source RISC-V CVA6 CPU	INVITED	HSTIEC
<b>SPCPS</b>				
1170	Dominik Marchsreiter and Johanna Sepúlveda	A PQC and QKD Hybridization for Quantum-Secure Communications	Long Presentation	SPCPS
1623	Fikret Basic, Christian Seifert, Christian Steger and Robert Köfler	Secure Data Acquisition for Battery Management Systems	Long Presentation	SPCPS
5286	Raphael Schermann, Rainer Urian and Christian Steger	Integration of the TPM in the AACKA protocol	Short Presentation	SPCPS
6848	Matthias Stammer, Matthias Hamann, Tanja Harbaum and Juergen Becker	Mitigating Masking in Automotive Communication Systems: Modeling and Hardware Generation	Long Presentation	SPCPS

7611	Mehrdad Poorhosseini and Kim Gruetner	A RISC-V based platform supporting mixed timing-critical and high performance workloads	Long Presentation	DCPS
------	---------------------------------------	---	-------------------	------

Best Paper Awards Candidats		
Authors	Title	Track
Mounika Vaddeboina, Endri Kaja, Alper Yilmazer, Sebastian Prebeck and Wolfgang Ecker	Parallel Golomb-Rice Decoder with 8-bit Unary decoding for Weight Compression in TinyML Applications	DSD
Juliano Pimentel, Alistair A. McEwan and Hong Qing Yu	A Novel Real-Time Framework for Embedded Systems Health Monitoring	DTFT
Arman Ferdowsi, Matthias Fuegger, Josef Salzmann and Ulrich Schmid	A Hybrid Delay Model for Interconnected Multi-Input Gates	DSD
<u>Pietro Fara, Gabriele Serra and Federico Aromolo</u>	Bounded transmission latency in real-time edge computing: a scheduling analysis	HSTIEC
Raquel Leon, Himar Fabelo, Samuel Ortega, Juan F. Piñeiro, Adam Szolna, Jesus Morera, Bernardino Clavo and Gustavo Marrero Callico	Evaluation of Hyperspectral Imaging Fusion for in-vivo Brain Tumor Identification and Delineation	HIAAA
Dominik Marchsreiter and Johanna Sepúlveda	A PQC and QKD Hybridization for Quantum-Secure Communications	SPCPS
Erling Jellum, Yaman Umuruglu, Milica Orlandic and Martin Schoeberl	fpga-tidbits: Rapid Prototyping of FPGA Accelerators in Chisel	DDVC